

Usuba  
High-Throughput and Constant-Time Ciphers,  
by Construction

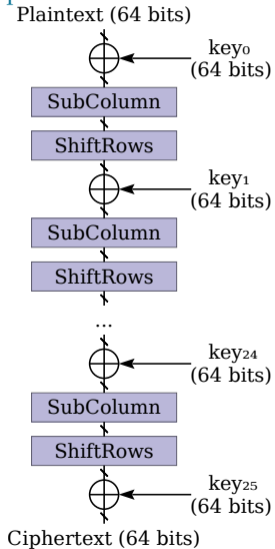
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June 24, 2019

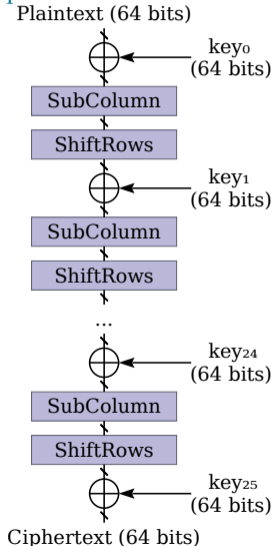
# Anatomy of a block cipher

## The Rectangle cipher



# Anatomy of a block cipher

## The Rectangle cipher

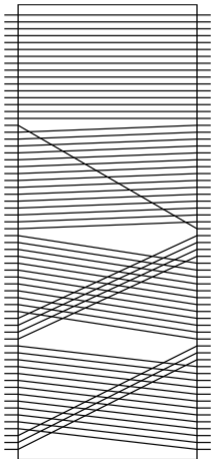


## USUBA

```
node Rectangle (plain:b64,  
                key :b64[26])  
    returns (cipher:b64)  
vars  
    round : b64[26]  
let  
    round[0] = plain;  
    forall i in [0,24] {  
        round[i+1] =  
            ShiftRows(  
                SubColumn(  
                    round[i] ^ key[i]  
                )  
            )  
    }  
    cipher = round[25] ^ key[25]  
tel
```

# Anatomy of a block cipher

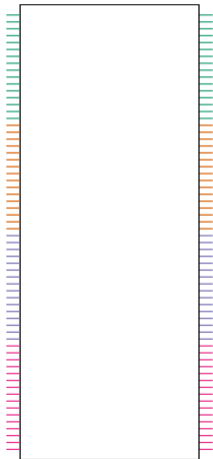
## Rectangle/ShiftRows



```
void ShiftRows(bool a[64], bool b[64]) {  
    b[0]  = a[0];  
    b[1]  = a[1];  
    b[2]  = a[2];  
    b[3]  = a[3];  
    b[4]  = a[4];  
    b[5]  = a[5];  
    ...  
    b[59] = a[56];  
    b[60] = a[57];  
    b[61] = a[58];  
    b[62] = a[59];  
    b[63] = a[60];  
}
```

# Anatomy of a block cipher

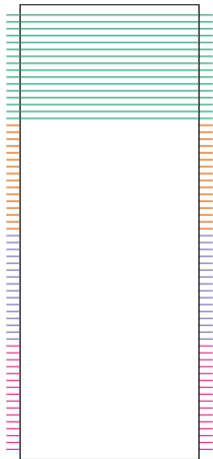
Rectangle/ShiftRows



```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
```

# Anatomy of a block cipher

## Rectangle/ShiftRows

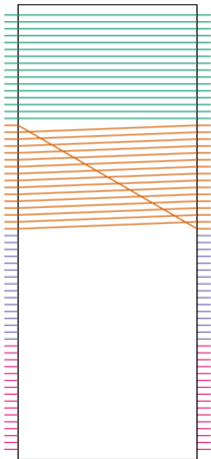


```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
let
  out[0] = input[0];

tel
```

# Anatomy of a block cipher

## Rectangle/ShiftRows

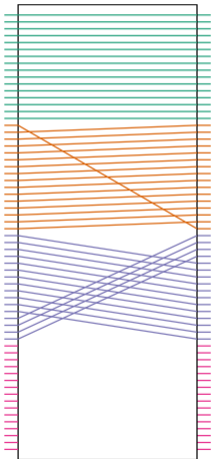


```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;

tel
```

# Anatomy of a block cipher

## Rectangle/ShiftRows

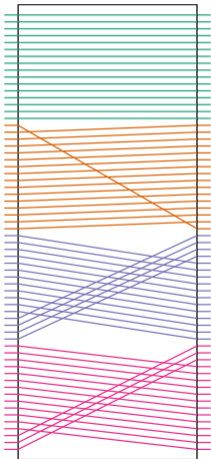


```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
tel
```



# Anatomy of a block cipher

## Rectangle/ShiftRows



```
node ShiftRows (input:u16x4)
  returns      (out:u16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13
tel
```

# Anatomy of a block cipher

## Rectangle/SubColumn

The S-box used in RECTANGLE is a 4-bit to 4-bit S-box  $S : F_2^4 \rightarrow F_2^4$ . The action of this S-box in hexadecimal notation is given by the following table.

$x$	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
$S(x)$	6	5	C	A	1	E	7	9	B	0	3	D	8	F	4	2

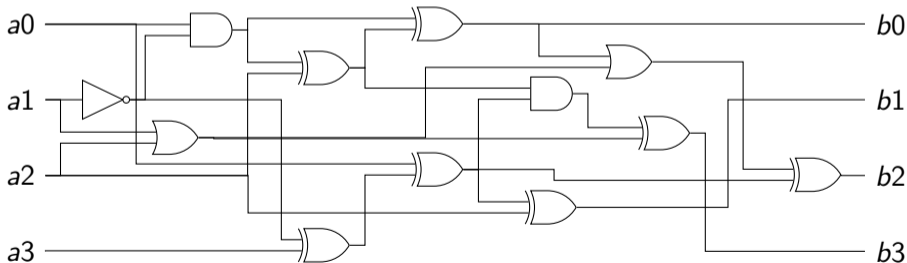
Caution: lookup tables are **strictly forbidden!**

# Anatomy of a block cipher

## Rectangle/SubColumn

The S-box used in RECTANGLE is a 4-bit to 4-bit S-box  $S : F_2^4 \rightarrow F_2^4$ . The action of this S-box in hexadecimal notation is given by the following table.

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# Anatomy of a block cipher

## Rectangle/SubColumn

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$x$	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
$S(x)$	6	5	C	A	1	E	7	9	B	0	3	D	8	F	4	2

```
void SubColumn(bool *a0, bool *a1,
               bool *a2, bool *a3) {
    bool t1, t2, t3, t5, t6, t8, t9, t11;
    bool a0_ = *a0;      bool a1_ = *a1;
    t1 = ~*a1;          t2 = *a0 & t1;          t3 = *a2 ^ *a3;
    *a0 = t2 ^ t3;      t5 = *a3 | t1;          t6 = a0_ ^ t5;
    *a1 = *a2 ^ t6;     t8 = a1_ ^ *a2;          t9 = t3 & t6;
    *a3 = t8 ^ t9;     t11 = *a0 | t8;          *a2 = t6 ^ t11;
}
```

# Anatomy of a block cipher

## Rectangle/SubColumn

The S-box used in RECTANGLE is a 4-bit to 4-bit S-box  $S : F_2^4 \rightarrow F_2^4$ . The action of this S-box in hexadecimal notation is given by the following table.

$x$	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
$S(x)$	6	5	C	A	1	E	7	9	B	0	3	D	8	F	4	2

```
table SubColumn (a:v4) returns (b:v4) {  
    6, 5, 12, 10, 1, 14, 7, 9, 11, 0, 3, 13, 8, 15, 4, 2  
}
```

# Anatomy of a block cipher

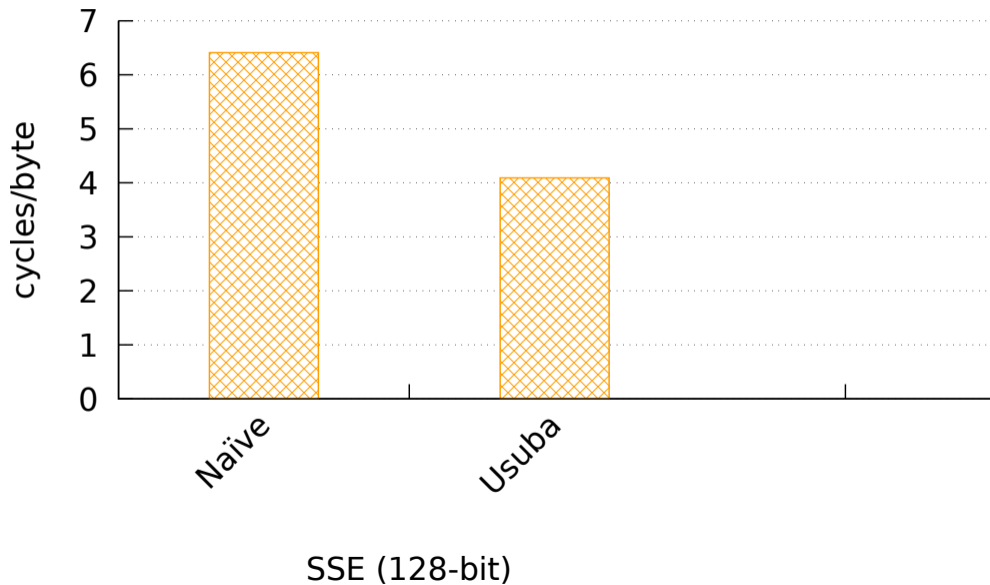
## Rectangle, our way

```
node ShiftRows (input:u16x4)
  returns (out:u16x4)
vars
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13
tel
```

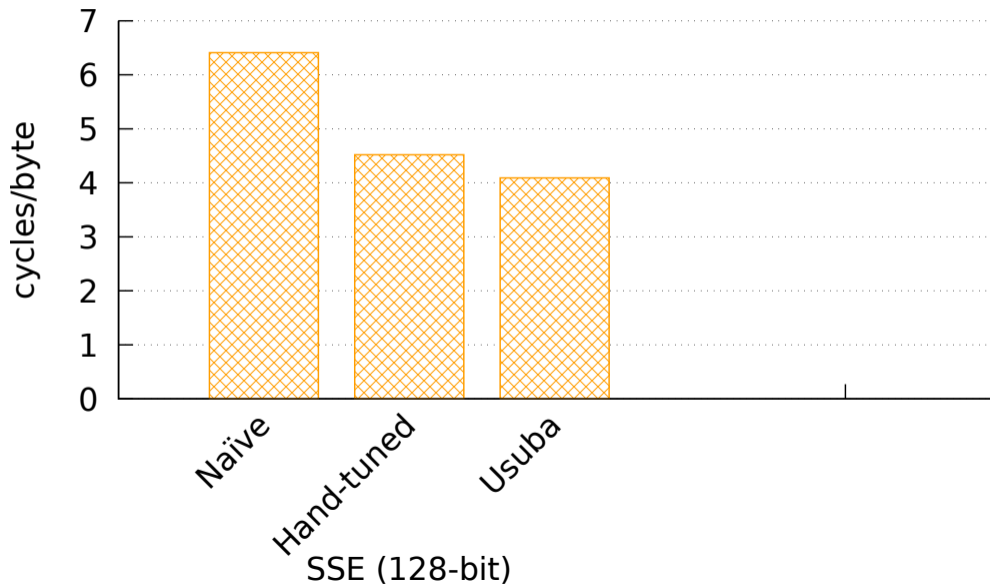
```
table SubColumn (input:v4)
  returns (out:v4) {
    6, 5, 12, 10, 1, 14, 7, 9,
    11, 0, 3, 13, 8, 15, 4, 2
  }
```

```
node Rectangle (plain:u16x4,
  key :u16x4[26])
  returns (cipher:u16x4)
vars
  round : u16x4[26]
let
  round[0] = plain;
  forall i in [0,24] {
    round[i+1] =
      ShiftRows(
        SubColumn(
          round[i] ^ key[i]
        )
      )
  }
  cipher = round[25] ^ key[25]
tel
```

## Man vs. Machine



## Man vs. Machine





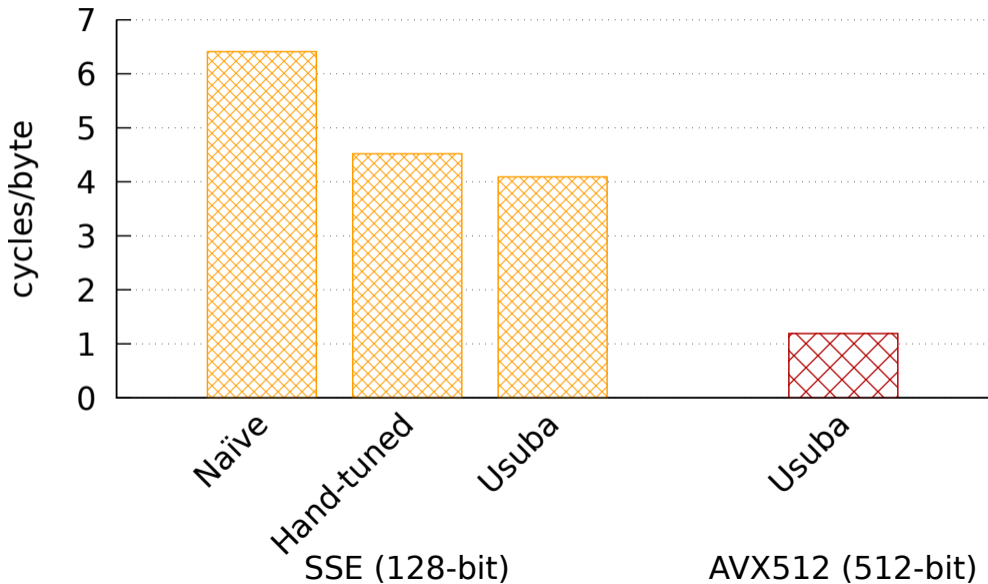
# Man vs. Machine

```
unsigned long r25 = p[9];
unsigned long r26 = p[17];
unsigned long r27 = p[25];
unsigned long r28 = p[33];
unsigned long r29 = p[41];
unsigned long r30 = p[49];
unsigned long r31 = p[57];

a1 (r31 ^ k[47], r0 ^ k[11], r1 ^ k[26], r2 ^ k[3], r3 ^ k[13],
r4 ^ k[41], r5 ^ k[16], r6 ^ k[130], r7 ^ k[4], r8 ^ k[11],
a2 (r3 ^ k[27], r4 ^ k[6], r5 ^ k[54], r6 ^ k[48], r7 ^ k[39],
r8 ^ k[19], r9 ^ k[12], r10 ^ k[11], r11 ^ k[17];
a3 (r7 ^ k[53], r8 ^ k[25], r9 ^ k[33], r10 ^ k[34], r11 ^ k[17],
r12 ^ k[5], r13 ^ k[15], r14 ^ k[18], r15 ^ k[15];
a4 (r11 ^ k[4], r12 ^ k[55], r13 ^ k[24], r14 ^ k[32], r15 ^ k[40],
r16 ^ k[20], r17 ^ k[125], r18 ^ k[19], r19 ^ k[10];
a5 (r15 ^ k[36], r16 ^ k[21], r17 ^ k[21], r18 ^ k[8], r19 ^ k[23],
r20 ^ k[52], r21 ^ k[17], r22 ^ k[124], r23 ^ k[12];
a6 (r19 ^ k[14], r20 ^ k[29], r21 ^ k[51], r22 ^ k[9], r23 ^ k[35],
r24 ^ k[20], r25 ^ k[28], r26 ^ k[110], r27 ^ k[18];
a7 (r23 ^ k[2], r24 ^ k[37], r25 ^ k[22], r26 ^ k[0], r27 ^ k[42],
r28 ^ k[38], r29 ^ k[11], r30 ^ k[121], r31 ^ k[7];
a8 (r27 ^ k[16], r28 ^ k[3], r29 ^ k[44], r30 ^ k[1], r31 ^ k[7],
r0 ^ k[28], r1 ^ k[12], r2 ^ k[114], r3 ^ k[120];
a1 (r31 ^ k[54], r0 ^ k[18], r1 ^ k[33], r2 ^ k[10], r3 ^ k[20],
r4 ^ k[48], r5 ^ k[16], r6 ^ k[22], r7 ^ k[30];
a2 (r3 ^ k[38], r4 ^ k[13], r5 ^ k[4], r6 ^ k[55], r7 ^ k[46],
r8 ^ k[26], r9 ^ k[12], r10 ^ k[17];
a3 (r7 ^ k[3], r8 ^ k[32], r9 ^ k[40], r10 ^ k[41], r11 ^ k[24],
r12 ^ k[12], r13 ^ k[15], r14 ^ k[29], r15 ^ k[5];
a4 (r11 ^ k[11], r12 ^ k[5], r13 ^ k[6], r14 ^ k[39], r15 ^ k[47],
r16 ^ k[27], r17 ^ k[9], r18 ^ k[0];
a5 (r15 ^ k[43], r16 ^ k[38], r17 ^ k[28], r18 ^ k[15], r19 ^ k[30],
r20 ^ k[0], r21 ^ k[13], r22 ^ k[24], r23 ^ k[2];
a6 (r19 ^ k[21], r20 ^ k[36], r21 ^ k[31], r22 ^ k[16], r23 ^ k[42],
r24 ^ k[137], r25 ^ k[10], r26 ^ k[18];
a7 (r23 ^ k[9], r24 ^ k[44], r25 ^ k[24], r26 ^ k[7], r27 ^ k[49],
r28 ^ k[45], r29 ^ k[1], r30 ^ k[6];
a8 (r27 ^ k[23], r28 ^ k[50], r29 ^ k[51], r30 ^ k[8], r31 ^ k[14],
r0 ^ k[35], r1 ^ k[26], r2 ^ k[40];
a1 (r31 ^ k[11], r0 ^ k[32], r1 ^ k[47], r2 ^ k[24], r3 ^ k[34],
r4 ^ k[5], r5 ^ k[116], r6 ^ k[130];
a2 (r3 ^ k[48], r4 ^ k[27], r5 ^ k[18], r6 ^ k[12], r7 ^ k[3],
r8 ^ k[40], r9 ^ k[12], r10 ^ k[17];
a3 (r7 ^ k[17], r8 ^ k[46], r9 ^ k[54], r10 ^ k[55], r11 ^ k[13],
r12 ^ k[26], r13 ^ k[15], r14 ^ k[15];
a4 (r11 ^ k[25], r12 ^ k[19], r13 ^ k[20], r14 ^ k[53], r15 ^ k[4],
r16 ^ k[41], r17 ^ k[125], r18 ^ k[19], r19 ^ k[10];
a5 (r15 ^ k[2], r16 ^ k[52], r17 ^ k[42], r18 ^ k[29], r19 ^ k[44],
r20 ^ k[14], r21 ^ k[13], r22 ^ k[12];
a6 (r19 ^ k[35], r20 ^ k[50], r21 ^ k[4], r22 ^ k[30], r23 ^ k[11],
r24 ^ k[51], r25 ^ k[13], r26 ^ k[118];
a7 (r23 ^ k[23], r24 ^ k[43], r25 ^ k[43], r26 ^ k[21], r27 ^ k[8],
r28 ^ k[0], r29 ^ k[11], r30 ^ k[46];
a8 (r27 ^ k[37], r28 ^ k[19], r29 ^ k[38], r30 ^ k[22], r31 ^ k[28],
r0 ^ k[49], r1 ^ k[126], r2 ^ k[114], r3 ^ k[120];
a1 (r31 ^ k[25], r0 ^ k[46], r1 ^ k[4], r2 ^ k[13], r3 ^ k[48],
r4 ^ k[19], r5 ^ k[16], r6 ^ k[22], r7 ^ k[30];
a2 (r3 ^ k[5], r4 ^ k[41], r5 ^ k[32], r6 ^ k[26], r7 ^ k[17],
r8 ^ k[54], r9 ^ k[12], r10 ^ k[17];
a3 (r7 ^ k[18], r8 ^ k[3], r9 ^ k[11], r10 ^ k[12], r11 ^ k[27],
r12 ^ k[40], r13 ^ k[15], r14 ^ k[29], r15 ^ k[5];
a4 (r11 ^ k[39], r12 ^ k[44], r13 ^ k[10], r14 ^ k[18],
r15 ^ k[55], r16 ^ k[19], r17 ^ k[0];
a5 (r15 ^ k[16], r16 ^ k[7], r17 ^ k[1], r18 ^ k[43], r19 ^ k[31],
r20 ^ k[28], r21 ^ k[3], r22 ^ k[2];
a6 (r19 ^ k[49], r20 ^ k[9], r21 ^ k[0], r22 ^ k[44], r23 ^ k[15],
r24 ^ k[38], r25 ^ k[28], r26 ^ k[12], r27 ^ k[35], r28 ^ k[22],
a7 (r23 ^ k[37], r24 ^ k[45], r25 ^ k[2], r26 ^ k[35], r27 ^ k[22],
r28 ^ k[4], r29 ^ k[21], r30 ^ k[6];
a8 (r27 ^ k[51], r28 ^ k[23], r29 ^ k[52], r30 ^ k[36], r31 ^ k[42],
r0 ^ k[8], r1 ^ k[26], r2 ^ k[4], r3 ^ k[27], r4 ^ k[5],
a1 (r31 ^ k[33], r0 ^ k[31], r1 ^ k[21], r2 ^ k[27], r3 ^ k[5],
r4 ^ k[33], r5 ^ k[18], r6 ^ k[122], r7 ^ k[130];
```

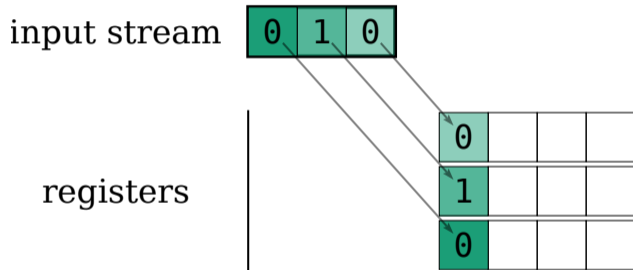
```
a2 (r3 ^ k[19], r4 ^ k[55], r5 ^ k[46], r6 ^ k[40], r7 ^ k[6],
r8 ^ k[11], r9 ^ k[127], r10 ^ k[11], r11 ^ k[17];
a3 (r7 ^ k[20], r8 ^ k[17], r9 ^ k[25], r10 ^ k[26], r11 ^ k[41],
r12 ^ k[54], r13 ^ k[15], r14 ^ k[129], r15 ^ k[5];
a4 (r11 ^ k[53], r12 ^ k[47], r13 ^ k[48], r14 ^ k[24], r15 ^ k[32],
r16 ^ k[12], r17 ^ k[125], r18 ^ k[19], r19 ^ k[10];
a5 (r15 ^ k[30], r16 ^ k[21], r17 ^ k[4], r18 ^ k[15],
r19 ^ k[42], r20 ^ k[17], r21 ^ k[13], r22 ^ k[124], r23 ^ k[12];
a6 (r19 ^ k[8], r20 ^ k[23], r21 ^ k[14], r22 ^ k[31], r23 ^ k[29],
r24 ^ k[4], r25 ^ k[110], r26 ^ k[18];
a7 (r23 ^ k[51], r24 ^ k[0], r25 ^ k[16], r26 ^ k[49], r27 ^ k[36],
r28 ^ k[38], r29 ^ k[13], r30 ^ k[11], r31 ^ k[16];
a8 (r27 ^ k[38], r28 ^ k[37], r29 ^ k[17], r30 ^ k[50], r31 ^ k[1],
r0 ^ k[28], r1 ^ k[12], r2 ^ k[114], r3 ^ k[120];
a1 (r31 ^ k[53], r0 ^ k[17], r1 ^ k[32], r2 ^ k[41], r3 ^ k[19],
r4 ^ k[47], r5 ^ k[16], r6 ^ k[22], r7 ^ k[30];
a2 (r3 ^ k[33], r4 ^ k[12], r5 ^ k[3], r6 ^ k[54], r7 ^ k[20],
r8 ^ k[25], r9 ^ k[12], r10 ^ k[17];
a3 (r7 ^ k[34], r8 ^ k[6], r9 ^ k[39], r10 ^ k[40], r11 ^ k[55],
r12 ^ k[11], r13 ^ k[25], r14 ^ k[29], r15 ^ k[5];
a4 (r11 ^ k[10], r12 ^ k[4], r13 ^ k[5], r14 ^ k[13], r15 ^ k[46],
r16 ^ k[26], r17 ^ k[35], r18 ^ k[9], r19 ^ k[0];
a5 (r15 ^ k[41], r16 ^ k[16], r17 ^ k[23], r18 ^ k[16], r19 ^ k[0],
r20 ^ k[3], r21 ^ k[7], r22 ^ k[24], r23 ^ k[2];
a6 (r19 ^ k[22], r20 ^ k[37], r21 ^ k[28], r22 ^ k[45], r23 ^ k[43],
r24 ^ k[7], r25 ^ k[3], r26 ^ k[18];
a7 (r23 ^ k[38], r24 ^ k[14], r25 ^ k[30], r26 ^ k[8], r27 ^ k[50],
r28 ^ k[42], r29 ^ k[1], r30 ^ k[6];
a8 (r27 ^ k[52], r28 ^ k[51], r29 ^ k[21], r30 ^ k[9], r31 ^ k[15],
r0 ^ k[36], r1 ^ k[26], r2 ^ k[4], r3 ^ k[13];
a1 (r31 ^ k[10], r0 ^ k[6], r1 ^ k[46], r2 ^ k[55], r3 ^ k[33],
r4 ^ k[4], r5 ^ k[116], r6 ^ k[130];
a2 (r3 ^ k[47], r4 ^ k[26], r5 ^ k[17], r6 ^ k[11], r7 ^ k[34],
r8 ^ k[39], r9 ^ k[127], r10 ^ k[11], r11 ^ k[17];
a3 (r7 ^ k[48], r8 ^ k[20], r9 ^ k[53], r10 ^ k[54], r11 ^ k[12],
r12 ^ k[25], r13 ^ k[15], r14 ^ k[129], r15 ^ k[5];
a4 (r11 ^ k[2], r12 ^ k[19], r13 ^ k[19], r14 ^ k[27], r15 ^ k[3],
r16 ^ k[40], r17 ^ k[125], r18 ^ k[19], r19 ^ k[10];
a5 (r15 ^ k[31], r16 ^ k[49], r17 ^ k[43], r18 ^ k[30], r19 ^ k[14],
r20 ^ k[17], r21 ^ k[13], r22 ^ k[12];
a6 (r19 ^ k[36], r20 ^ k[51], r21 ^ k[42], r22 ^ k[0], r23 ^ k[2],
r24 ^ k[21], r25 ^ k[13], r26 ^ k[110], r27 ^ k[18];
a7 (r23 ^ k[21], r24 ^ k[44], r25 ^ k[44], r26 ^ k[22], r27 ^ k[9],
r28 ^ k[3], r29 ^ k[11], r30 ^ k[16];
a8 (r27 ^ k[7], r28 ^ k[38], r29 ^ k[35], r30 ^ k[23], r31 ^ k[29],
r0 ^ k[50], r1 ^ k[126], r2 ^ k[114], r3 ^ k[120];
a1 (r31 ^ k[24], r0 ^ k[20], r1 ^ k[31], r2 ^ k[12], r3 ^ k[47],
r4 ^ k[18], r5 ^ k[16], r6 ^ k[22], r7 ^ k[30];
a2 (r3 ^ k[4], r4 ^ k[40], r5 ^ k[6], r6 ^ k[25], r7 ^ k[48],
r8 ^ k[13], r9 ^ k[27], r10 ^ k[17];
a3 (r7 ^ k[5], r8 ^ k[34], r9 ^ k[10], r10 ^ k[11], r11 ^ k[26],
r12 ^ k[39], r13 ^ k[15], r14 ^ k[29], r15 ^ k[5];
a4 (r11 ^ k[54], r12 ^ k[19], r13 ^ k[13], r14 ^ k[41], r15 ^ k[17],
r16 ^ k[45], r17 ^ k[8], r18 ^ k[2], r19 ^ k[28], r20 ^ k[2];
a5 (r15 ^ k[50], r16 ^ k[38], r17 ^ k[11], r18 ^ k[14], r19 ^ k[16],
r20 ^ k[35], r21 ^ k[28], r22 ^ k[10], r23 ^ k[18];
a6 (r19 ^ k[7], r20 ^ k[42], r21 ^ k[31], r22 ^ k[36], r23 ^ k[23],
r24 ^ k[15], r25 ^ k[13], r26 ^ k[2], r27 ^ k[5], r28 ^ k[4];
a7 (r23 ^ k[21], r24 ^ k[52], r25 ^ k[49], r26 ^ k[37], r27 ^ k[43],
r28 ^ k[19], r29 ^ k[26], r30 ^ k[14], r31 ^ k[20];
a1 (r31 ^ k[6], r0 ^ k[27], r1 ^ k[13], r2 ^ k[19], r3 ^ k[54],
r4 ^ k[25], r5 ^ k[116], r6 ^ k[130];
a2 (r3 ^ k[11], r4 ^ k[47], r5 ^ k[13], r6 ^ k[32], r7 ^ k[55],
r8 ^ k[31], r9 ^ k[127], r10 ^ k[11], r11 ^ k[17];
a3 (r7 ^ k[12], r8 ^ k[41], r9 ^ k[17], r10 ^ k[18], r11 ^ k[33],
r12 ^ k[46], r13 ^ k[15], r14 ^ k[129], r15 ^ k[5];
a4 (r11 ^ k[20], r12 ^ k[39], r13 ^ k[40], r14 ^ k[48], r15 ^ k[24],
r16 ^ k[15], r17 ^ k[125], r18 ^ k[19], r19 ^ k[10];
a5 (r15 ^ k[52], r16 ^ k[15], r17 ^ k[19], r18 ^ k[51], r19 ^ k[35],
r20 ^ k[36], r21 ^ k[13], r22 ^ k[124], r23 ^ k[12];
a6 (r19 ^ k[2], r20 ^ k[42], r21 ^ k[18], r22 ^ k[21], r23 ^ k[23],
```

# Man vs. Machine



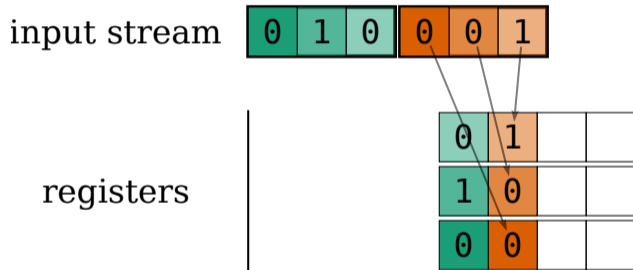
# Bitslicing

High-throughput software circuits



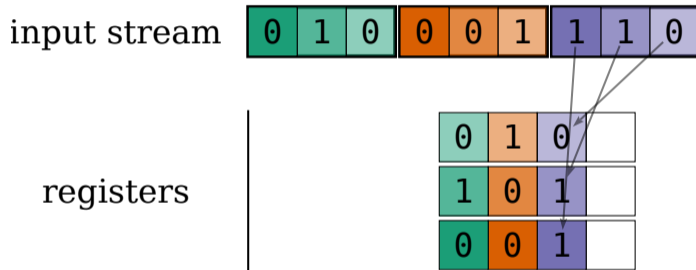
# Bitslicing

High-throughput software circuits



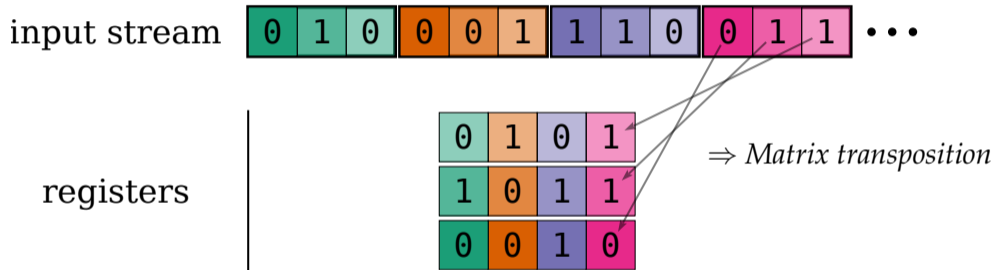
# Bitslicing

High-throughput software circuits



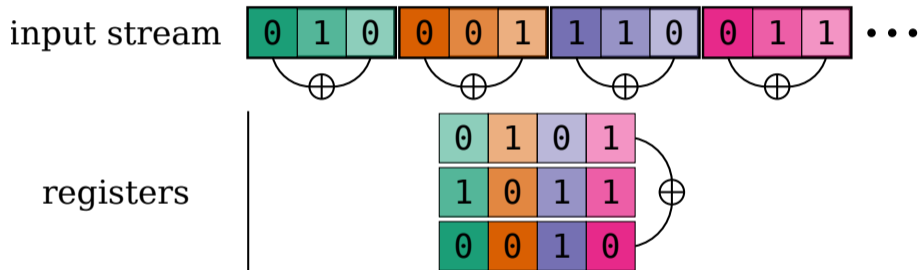
# Bitslicing

High-throughput software circuits



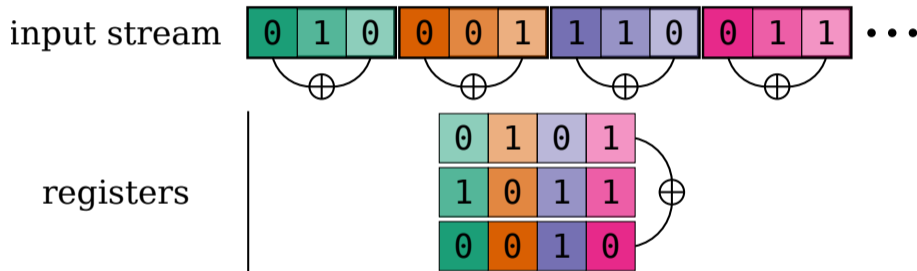
# Bitslicing

High-throughput software circuits



# Bitslicing

High-throughput software circuits

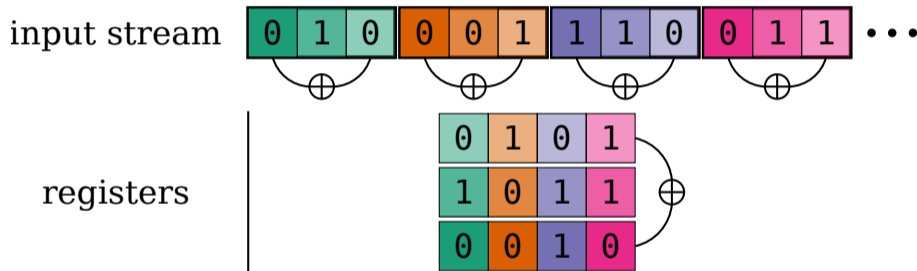


*Wider registers  $\Rightarrow$  More parallelism  
(SSE, AVX, AVX2, AVX-512, ...)*



# Bitslicing

High-throughput software circuits



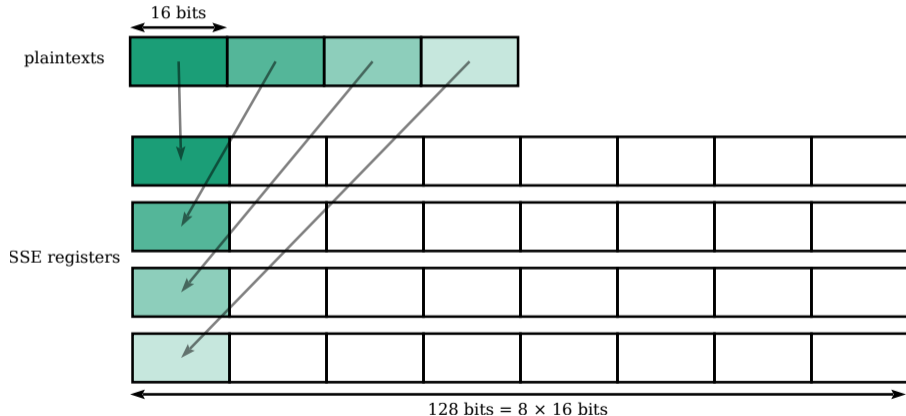
*Wider registers  $\Rightarrow$  More parallelism  
(SSE, AVX, AVX2, AVX-512, ...)*

*Register pressure?*

# V-slicing

## ShiftRows in Vertical mode

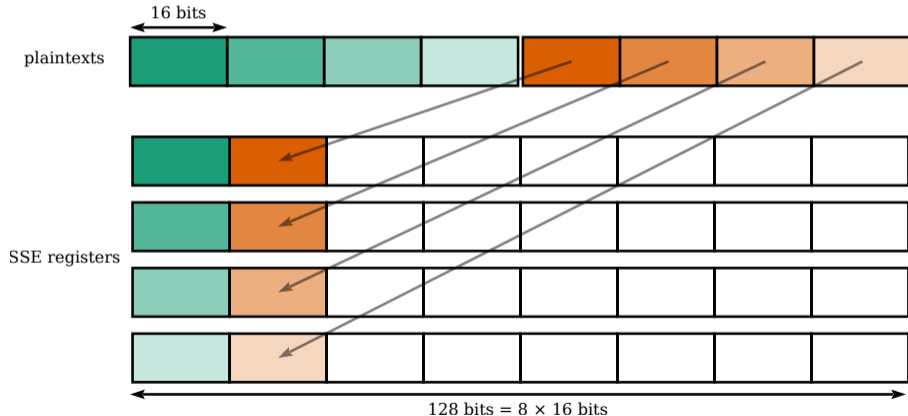
```
node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
     out[1] = input[1] <<< 1;
     out[2] = input[2] <<< 12;
     out[3] = input[3] <<< 13;      tel
```



# V-slicing

## ShiftRows in Vertical mode

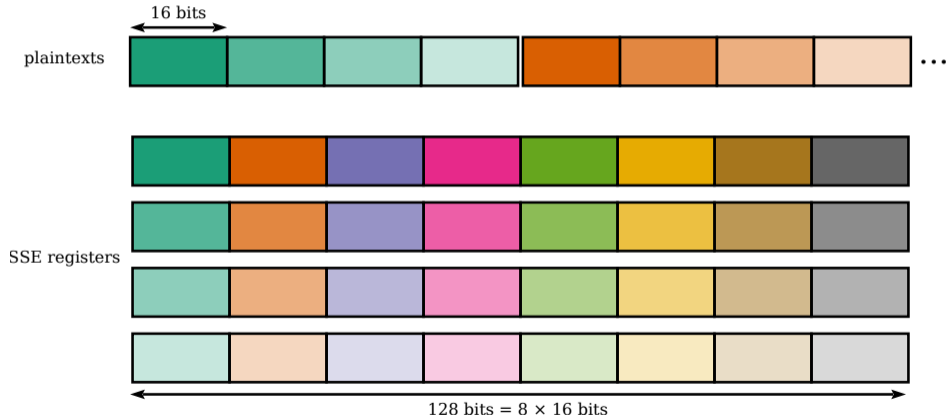
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node ShiftRows (input:u16x4) : (out:u16x4)
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# V-slicing

## ShiftRows in Vertical mode

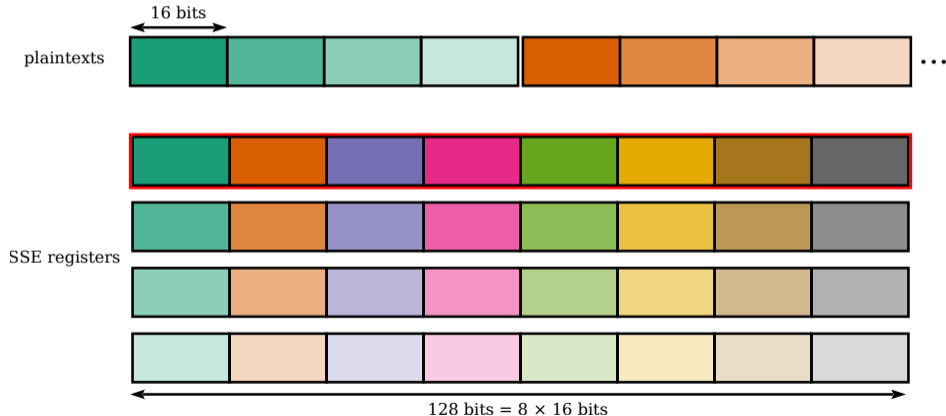
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node ShiftRows (input:u16x4) : (out:u16x4)
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     out[1] = input[1] <<< 1;
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# V-slicing

## ShiftRows in Vertical mode

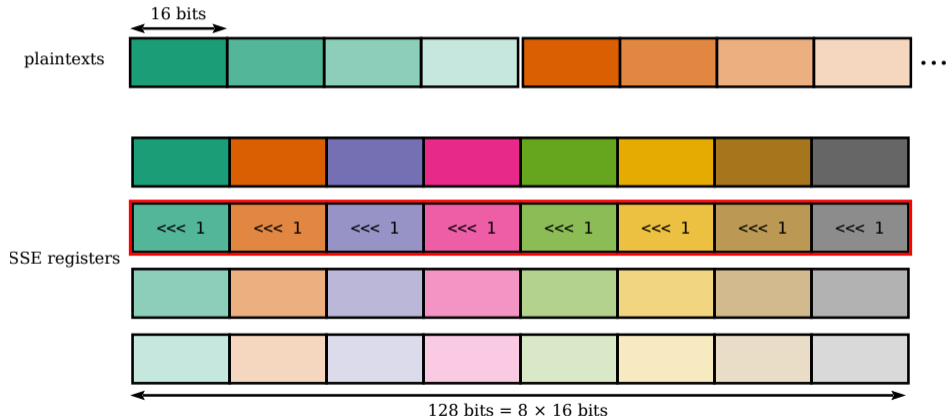
```
node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
     out[1] = input[1] <<< 1;
     out[2] = input[2] <<< 12;
     out[3] = input[3] <<< 13;      tel
```



# V-slicing

## ShiftRows in Vertical mode

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let  out[0] = input[0];
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     out[2] = input[2] <<< 12;
     out[3] = input[3] <<< 13;      tel
```

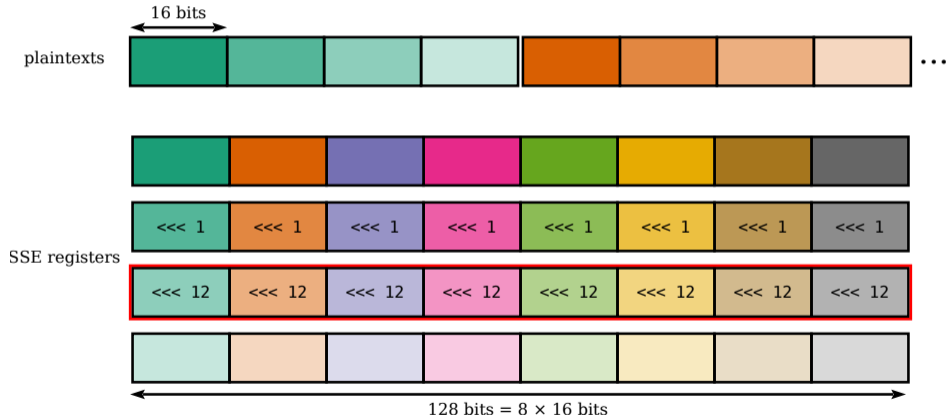


```
_mm_slli_epi16, _mm_or_si128, __mm_srli_epi16
```

# V-slicing

## ShiftRows in Vertical mode

```
node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
     out[1] = input[1] <<< 1;
     out[2] = input[2] <<< 12;
     out[3] = input[3] <<< 13;      tel
```

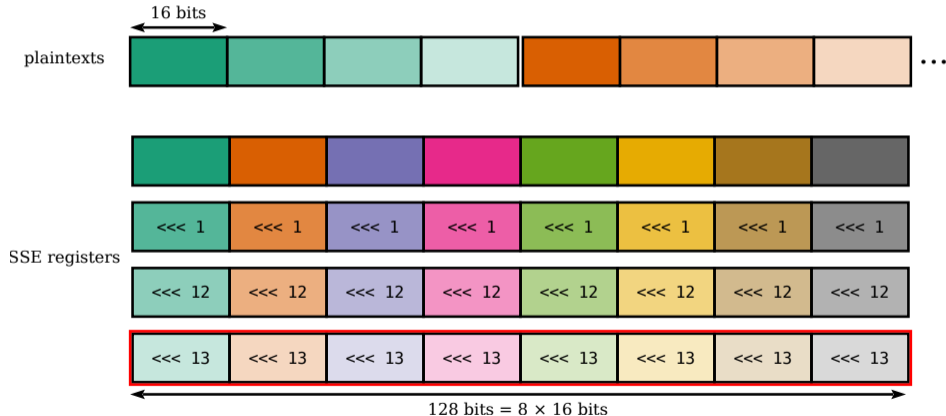


```
_mm_slli_epi16, _mm_or_si128, __mm_srli_epi16
```

# V-slicing

## ShiftRows in Vertical mode

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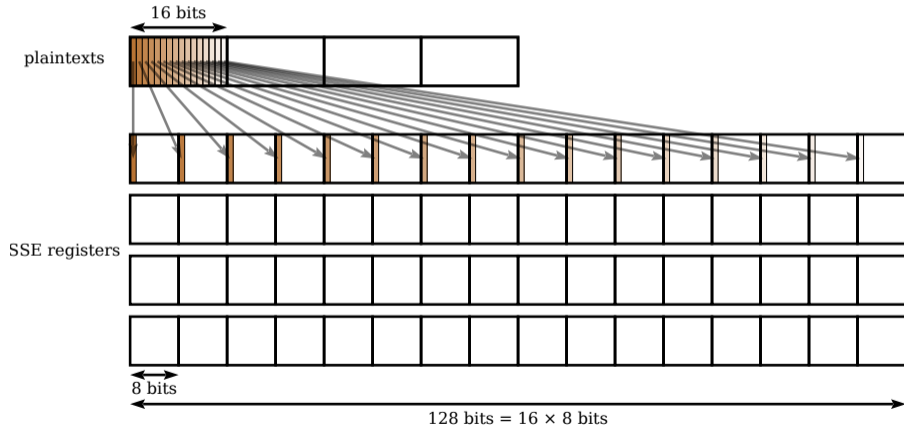
```
_mm_slli_epi16, _mm_or_si128, __mm_srli_epi16
```



# H-slicing

## ShiftRows in Horizontal mode

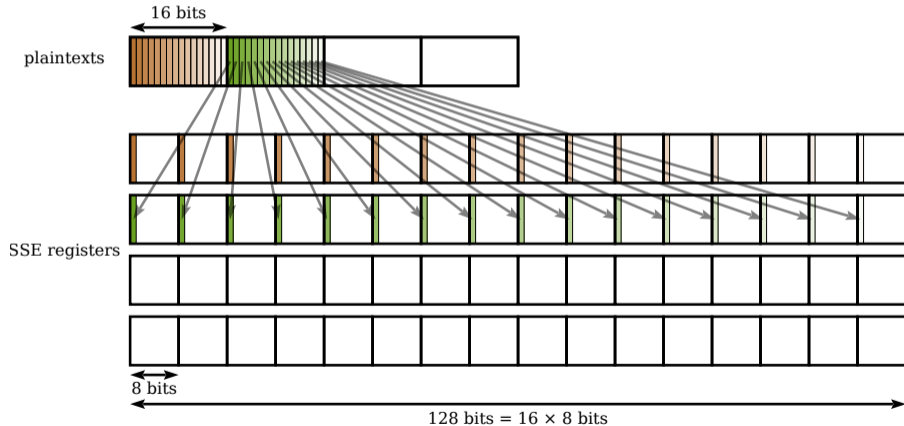
```
node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
     out[1] = input[1] <<< 1;
     out[2] = input[2] <<< 12;
     out[3] = input[3] <<< 13;      tel
```



# H-slicing

## ShiftRows in Horizontal mode

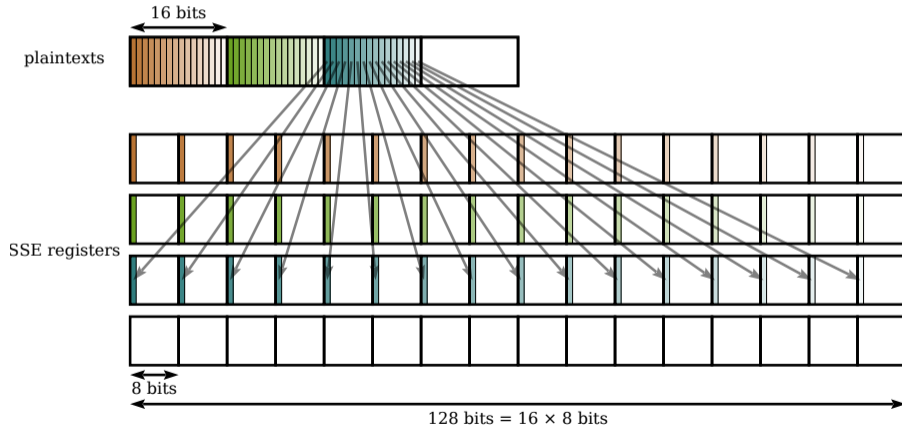
```
node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
     out[1] = input[1] <<< 1;
     out[2] = input[2] <<< 12;
     out[3] = input[3] <<< 13;      tel
```



# H-slicing

## ShiftRows in Horizontal mode

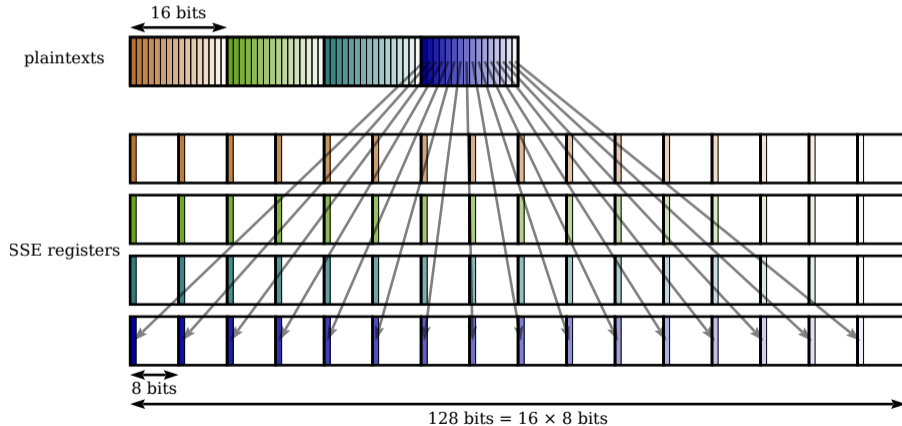
```
node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
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     out[2] = input[2] <<< 12;
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# H-slicing

## ShiftRows in Horizontal mode

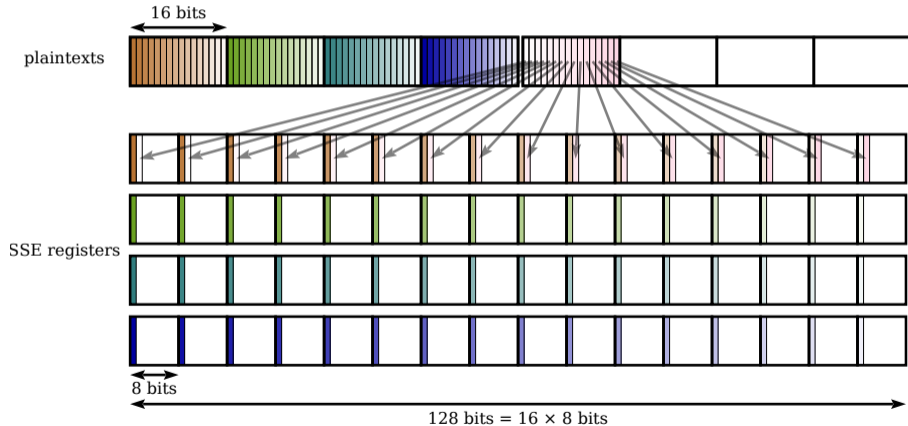
```
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# H-slicing

## ShiftRows in Horizontal mode

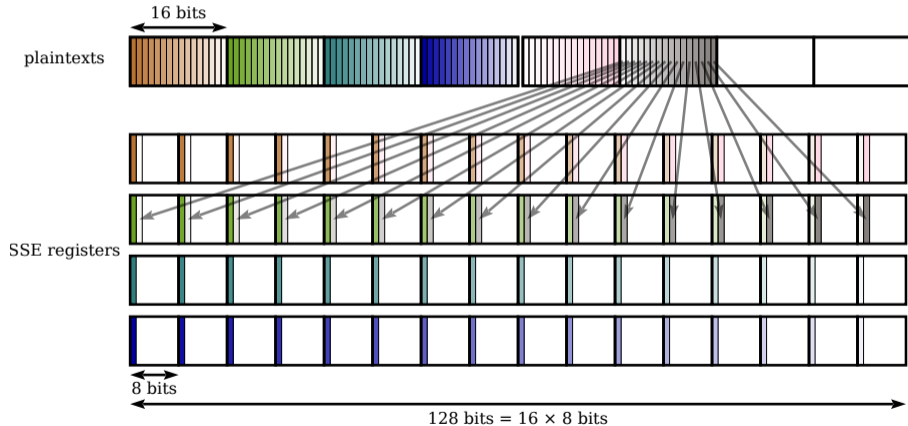
```
node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
     out[1] = input[1] <<< 1;
     out[2] = input[2] <<< 12;
     out[3] = input[3] <<< 13;      tel
```



# H-slicing

## ShiftRows in Horizontal mode

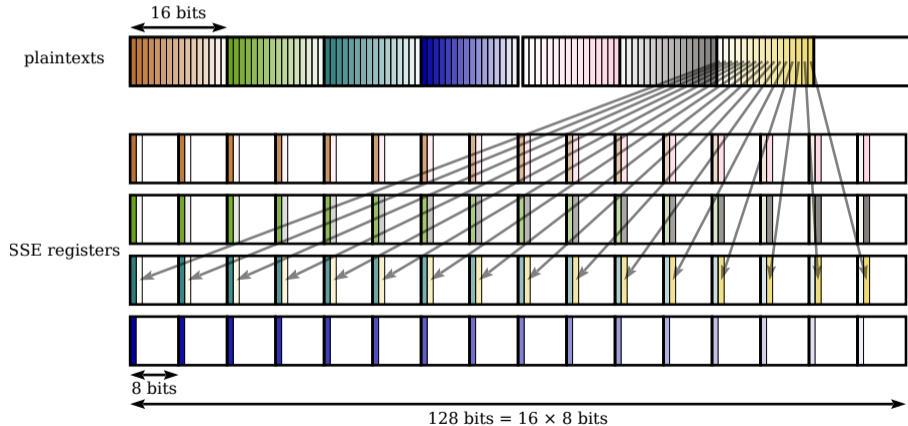
```
node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
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     out[2] = input[2] <<< 12;
     out[3] = input[3] <<< 13;      tel
```



# H-slicing

## ShiftRows in Horizontal mode

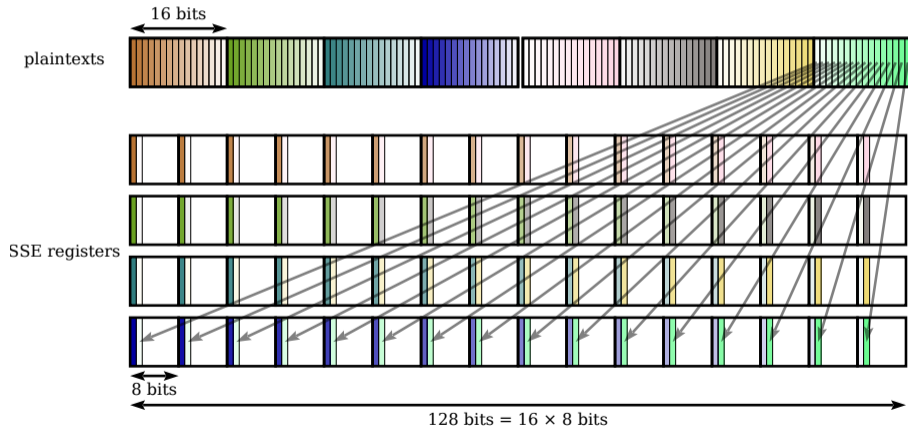
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# H-slicing

## ShiftRows in Horizontal mode

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node ShiftRows (input:u16x4) : (out:u16x4)
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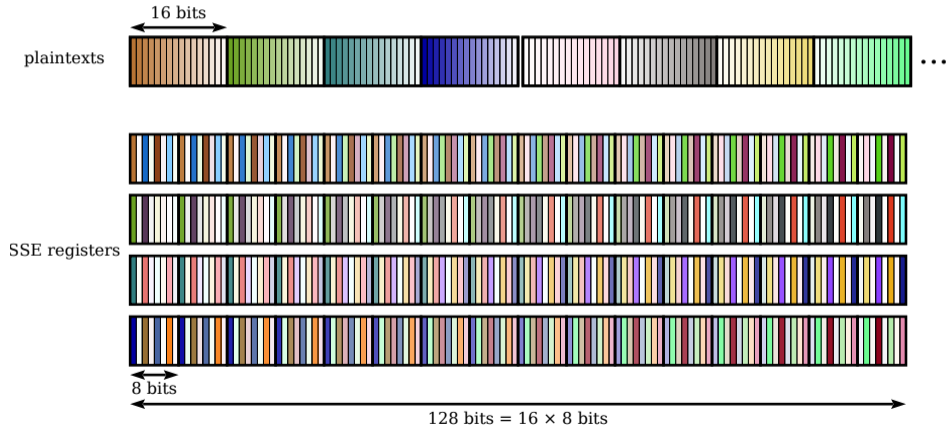




# H-slicing

## ShiftRows in Horizontal mode

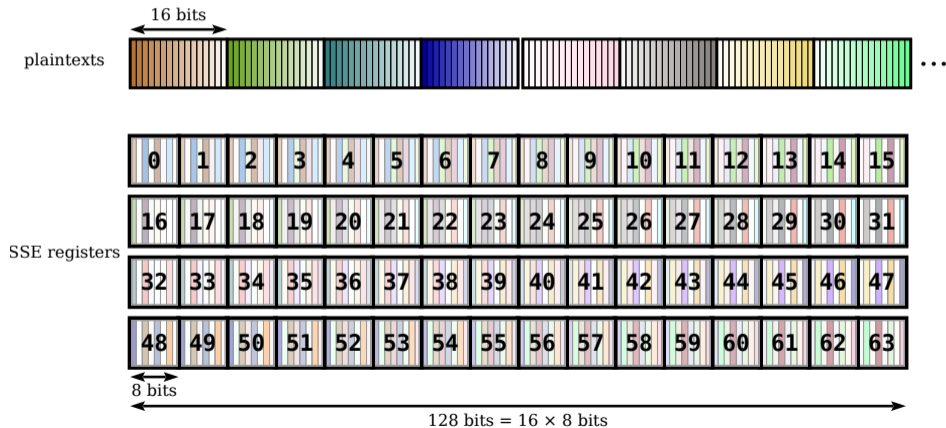
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node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
     out[1] = input[1] <<< 1;
     out[2] = input[2] <<< 12;
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```



# H-slicing

## ShiftRows in Horizontal mode

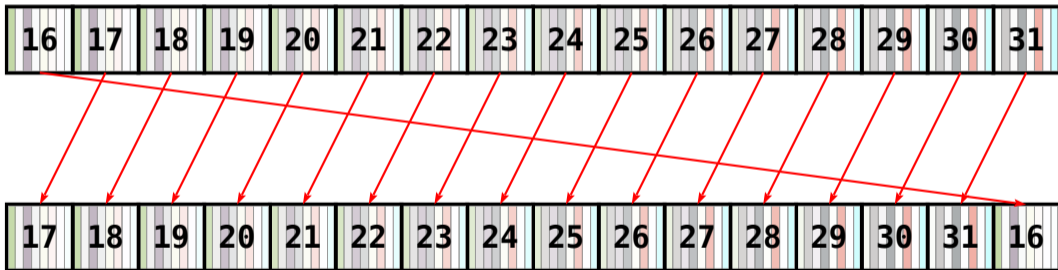
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# H-slicing

ShiftRows in Horizontal mode

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     out[3] = input[3] <<< 13;      tel
```

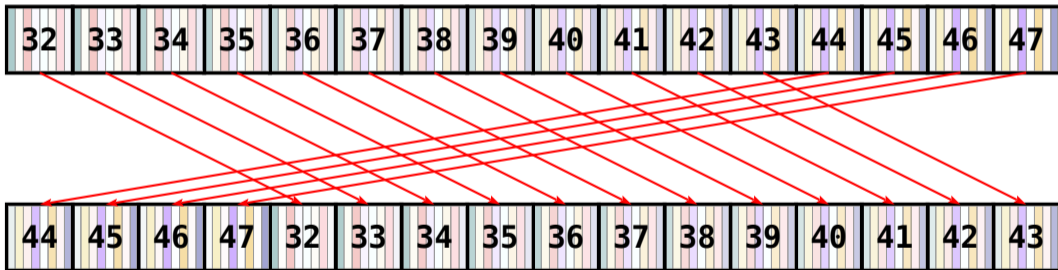


```
__m128i _mm_shuffle_epi8 (__m128i a, __m128i b)
```

# H-slicing

ShiftRows in Horizontal mode

```
node ShiftRows (input:u16x4) : (out:u16x4)
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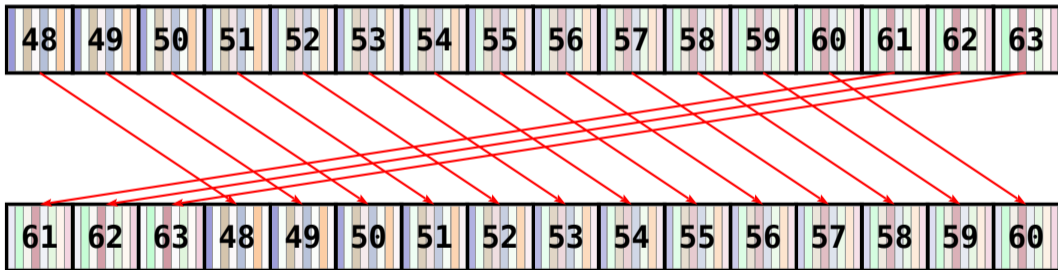


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```

# H-slicing

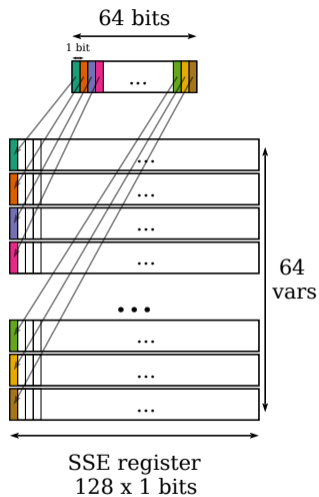
ShiftRows in Horizontal mode

```
node ShiftRows (input:u16x4) : (out:u16x4)
let  out[0] = input[0];
     out[1] = input[1] <<< 1;
     out[2] = input[2] <<< 12;
     out[3] = input[3] <<< 13;      tel
```



```
__m128i _mm_shuffle_epi8 (__m128i a, __m128i b)
```

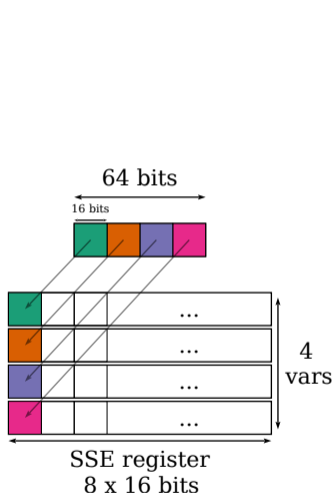
# Parallelization strategies



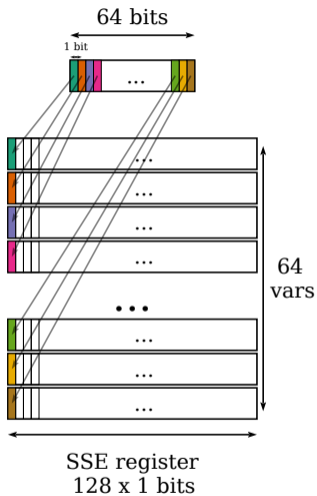
bitslicing

b64

# Parallelization strategies

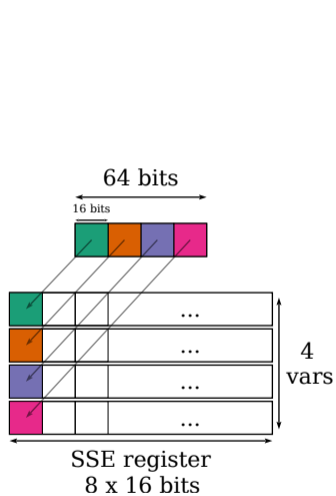


vslicing  
 $u_V 16 \times 4$

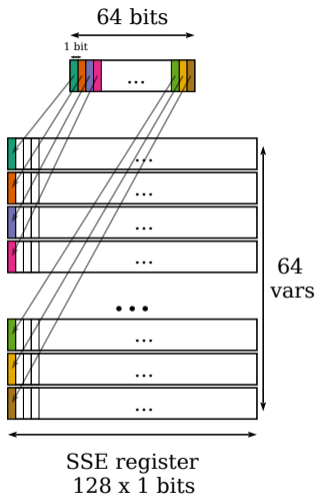


bitslicing  
 $b_{64}$

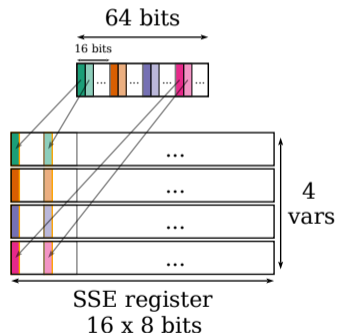
# Parallelization strategies



vslicing  
 $u_V 16x4$



bitslicing  
 $b_{64}$



hslicing  
 $u_H 16x4$



## Quick Peek at the Language

```
node ShiftRows (input:u16x4)
  returns (output:u16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13
tel
```

## Quick Peek at the Language

```
node ShiftRows (input:u16x4)
  returns (output:u16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13
tel
```

## Quick Peek at the Language

```
node ShiftRows (input:uD16x4)
  returns (output:uD16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13
tel
```

## Quick Peek at the Language

```
node ShiftRows (input:uD16x4)
  returns (output:uD16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13
tel
```

# Quick Peek at the Language

```
node ShiftRows (input:uv16x4)
  returns (output:uv16x4)
let
  out[0] = input[0];
  out[1] = input[1] <<< 1;
  out[2] = input[2] <<< 12;
  out[3] = input[3] <<< 13;
tel
```

*vslicing*

shifts

# Quick Peek at the Language

```
node ShiftRows (input:uH16x4)  
  returns (output:uH16x4)  
let  
  out[0] = input[0];  
  out[1] = input[1] <<< 1;  
  out[2] = input[2] <<< 12;  
  out[3] = input[3] <<< 13  
tel
```

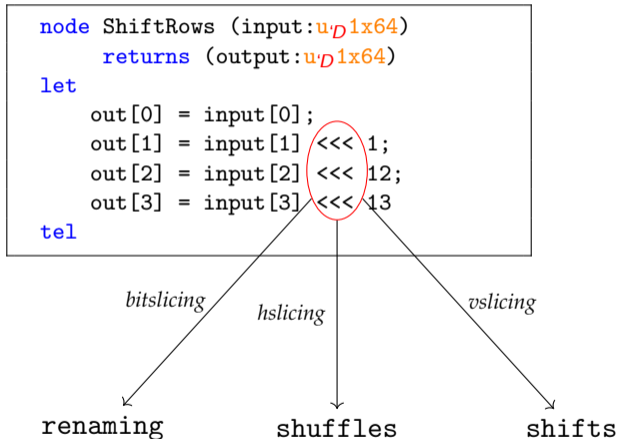
*hslicing*

shuffles

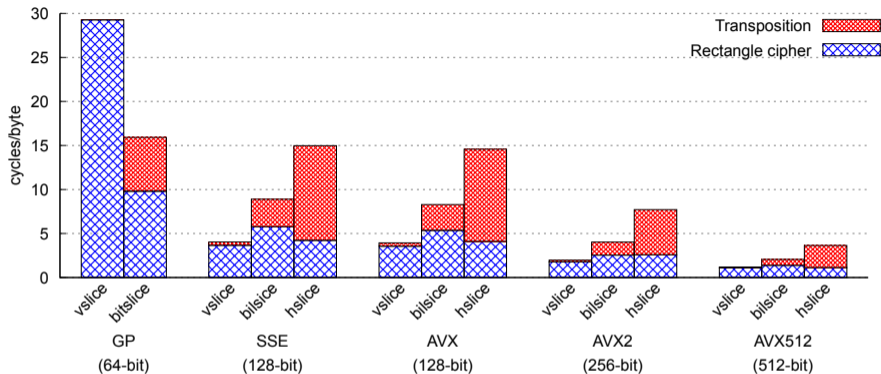
*vslicing*

shifts

# Quick Peek at the Language



# Monomorphization



```
node Rectangle (plain : u16x4, key : u16x4[26]) returns (cipher : u16x4)
```

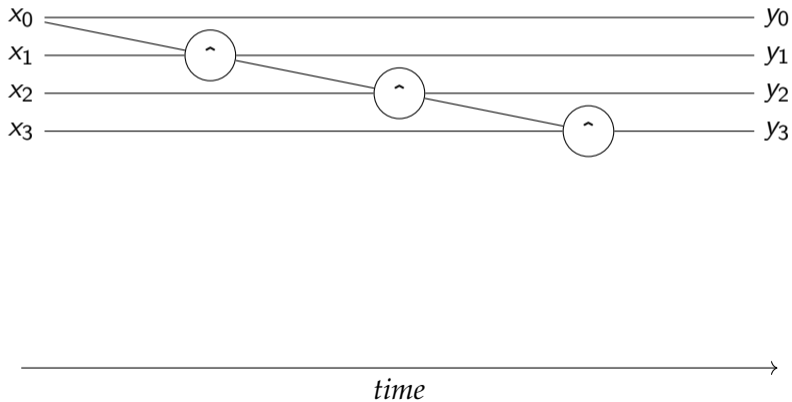
```
void RectangleV (__m256i plain[4], __m256i key[26][4], __m256i cipher[4])  
void RectangleB (__m128i plain[64], __m128i key[26][64], __m128i cipher[64])
```



# Interleaving

Exploiting superscalar CPUs

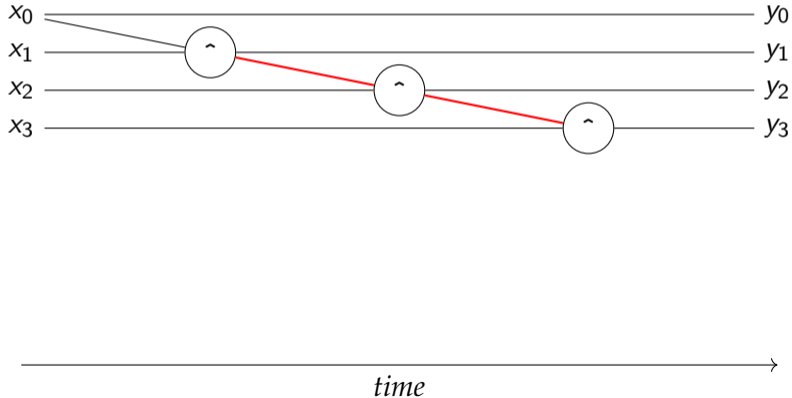
```
node my_cipher (x:b4) returns (y:b4)
let   y[0] = x[0];
      forall i in [1, 3] {
        y[i] = y[i-1] ^ x[i];
      }
tel
```



# Interleaving

Exploiting superscalar CPUs

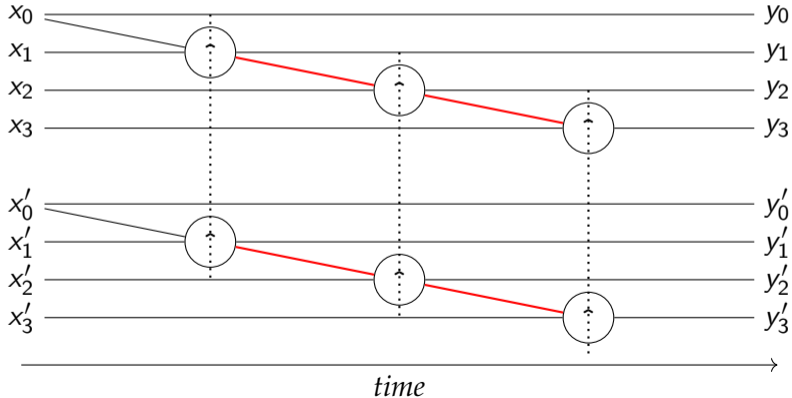
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let   y[0] = x[0];
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        y[i] = y[i-1] ^ x[i];
      }
tel
```



# Interleaving

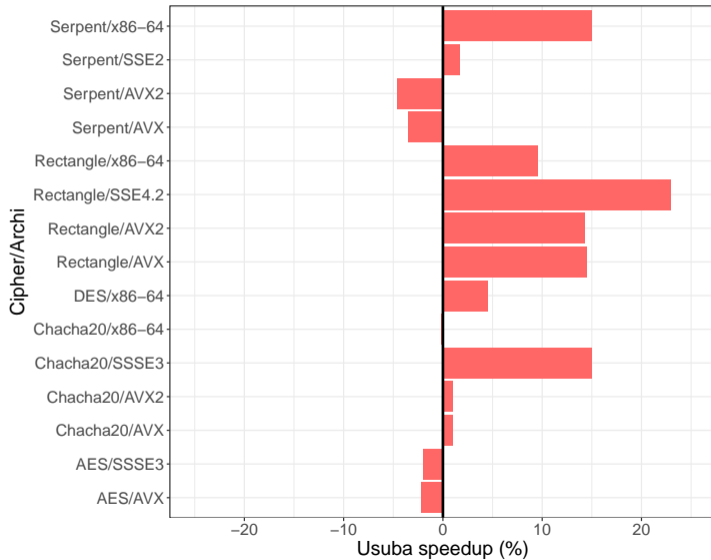
Exploiting superscalar CPUs

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let   y[0] = x[0];
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      }
tel
```



# Evaluation

Usuba *vs.* Reference



# Conclusion

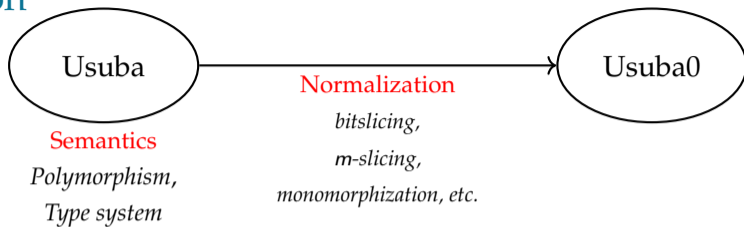
Usuba

Semantics

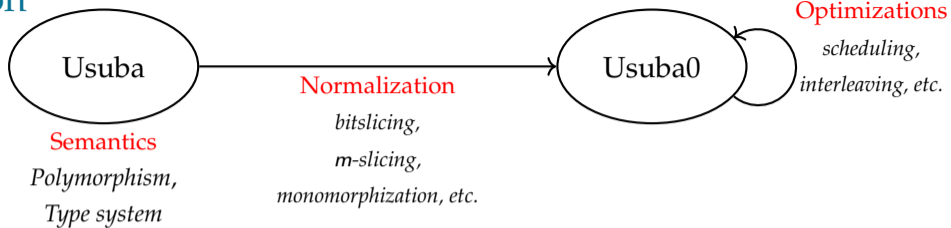
*Polymorphism,*

*Type system*

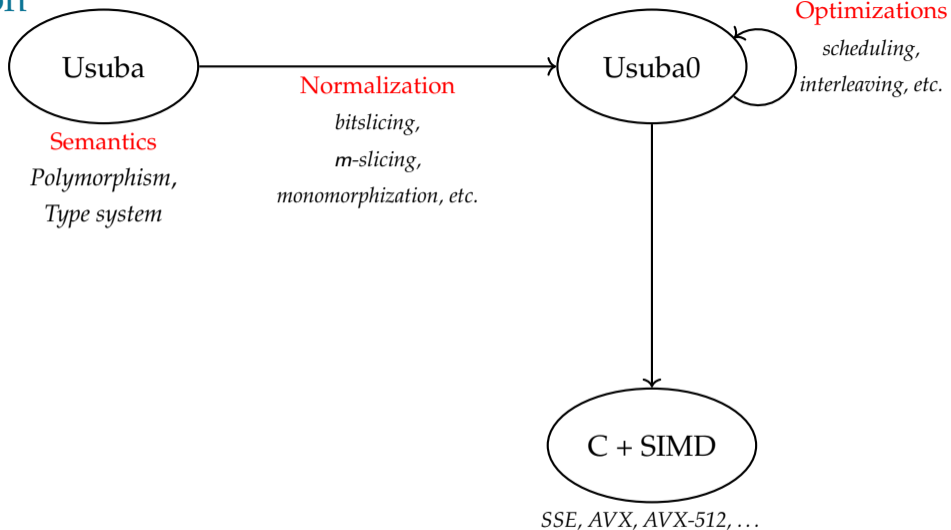
# Conclusion



# Conclusion

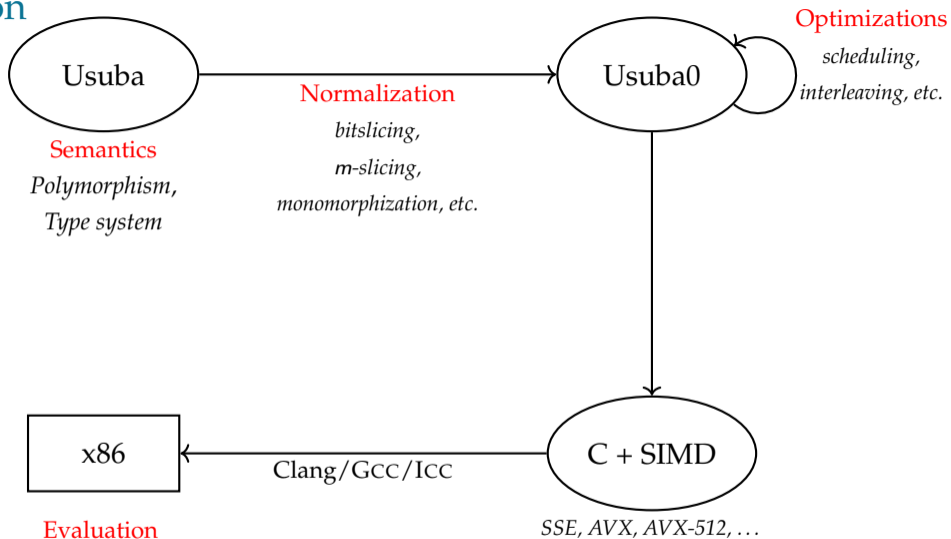


# Conclusion

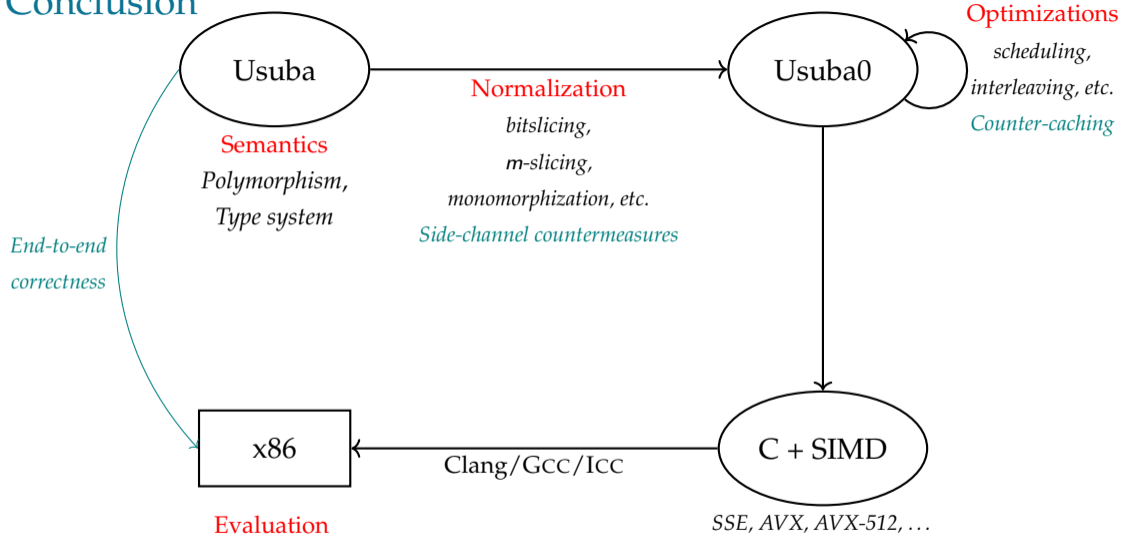




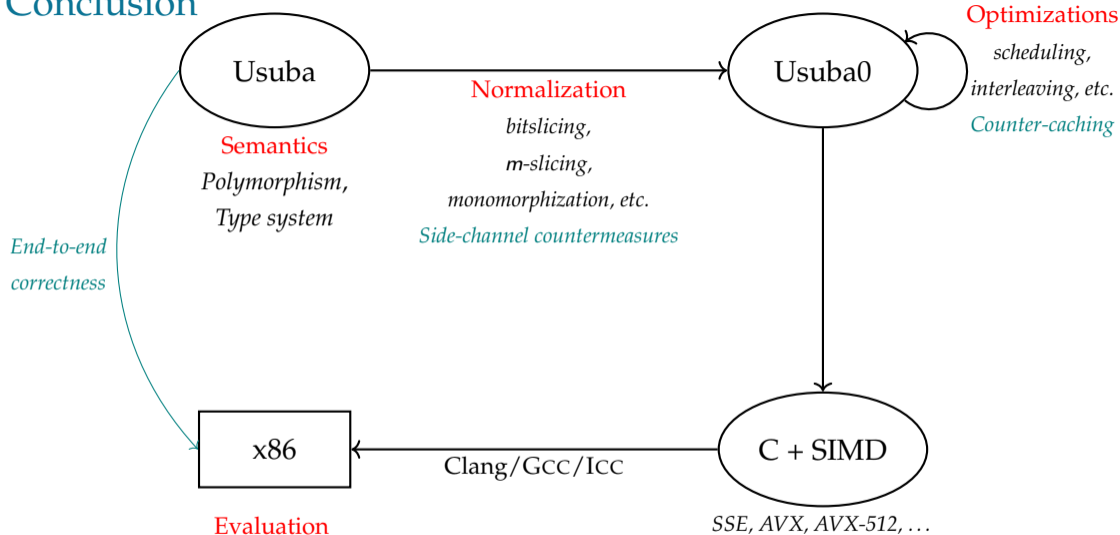
# Conclusion



# Conclusion



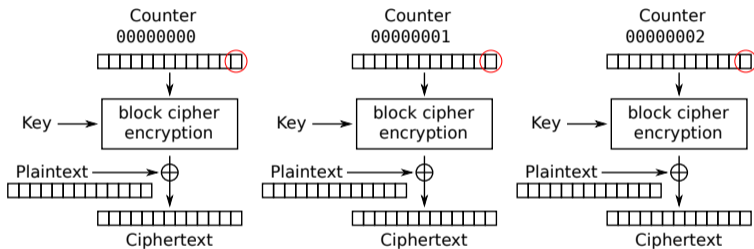
# Conclusion



[github.com/DadaIsCrazy/Usuba](https://github.com/DadaIsCrazy/Usuba)

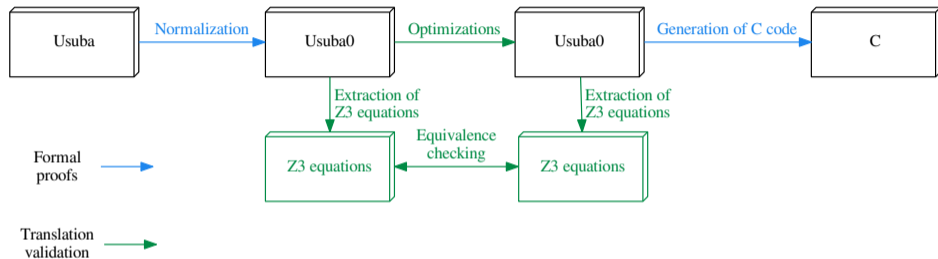
# Future work

## Optimization: counter caching



# Future work

## Verification



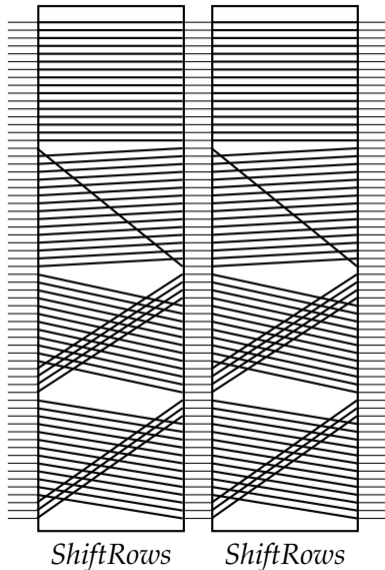
- Validate compilation
- Validate manual modifications

## Design space

Cipher	Mode	CC	Inline	Unroll	Interleave	Schedule
DES	bitslice	Clang	✓	✓		✓
AES	bitslice	Clang	✓	✓		✓
	hslice	Clang	✓	✓		✓
Rectangle	bitslice	ICC	✓	✓		✓
	hslice	GCC			✓	✓
	vslice	Clang			✓	✓
Chacha20	vslice	ICC	✓	✓		✓
Serpent	vslice	Clang		✓	✓	

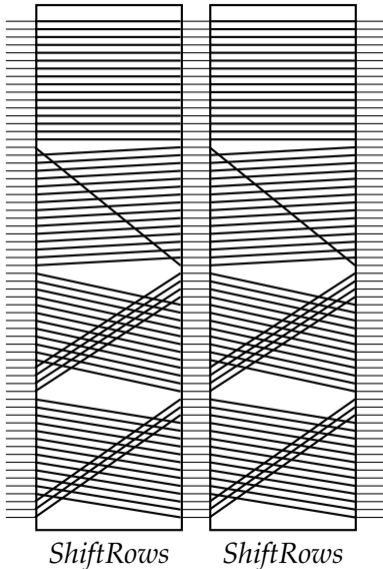
# Unrolling & Inlining

```
node ShiftRows_x2 (plain:b64)
    returns (cipher:b64)
let
    forall i in [0,1] {
        plain = ShiftRows(plain)
    }
    cipher = plain
tel
```



# Unrolling & Inlining

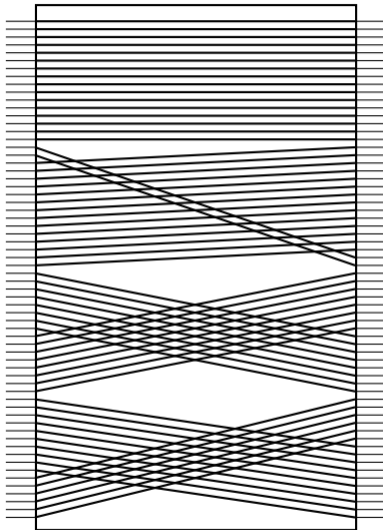
```
node ShiftRows_x2 (plain:b64)
    returns (cipher:b64)
let
  forall i in [0,1] {
    tmp[0] = plain[0];
    tmp[1] = plain[1];
    ...
    tmp[16] = plain[17];
    tmp[17] = plain[18];
    ...
    tmp[63] = plain[60];
    plain = tmp;
  }
  cipher = plain
tel
```





# Unrolling & Inlining

```
node ShiftRows_x2 (plain:b64)
    returns (cipher:b64)
let
    cipher[0] = plain[0];
    cipher[1] = plain[1];
    ...
    cipher[16] = plain[18];
    cipher[17] = plain[19];
    ...
    cipher[63] = plain[57];
tel
```



*ShiftRows (x2)*

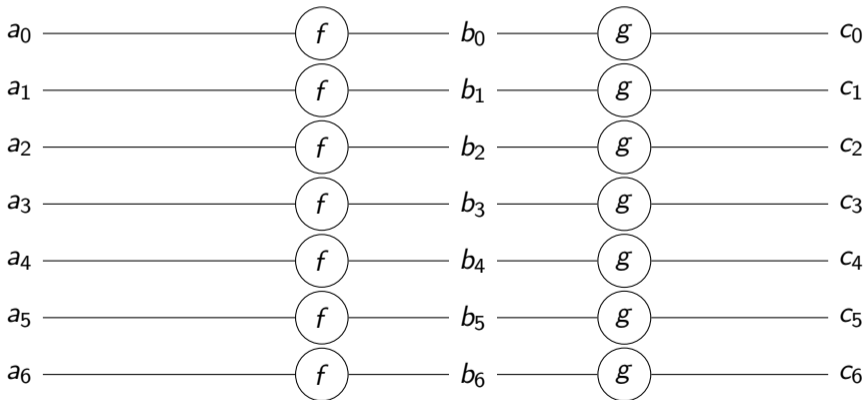
## Scheduling bitsliced code

```
// Suppose f: b1 -> b1 and g: b1 -> b1  
node my_cipher (a:b7) returns (c:b7)  
let   b = f(a);  
      c = g(b);   tel
```



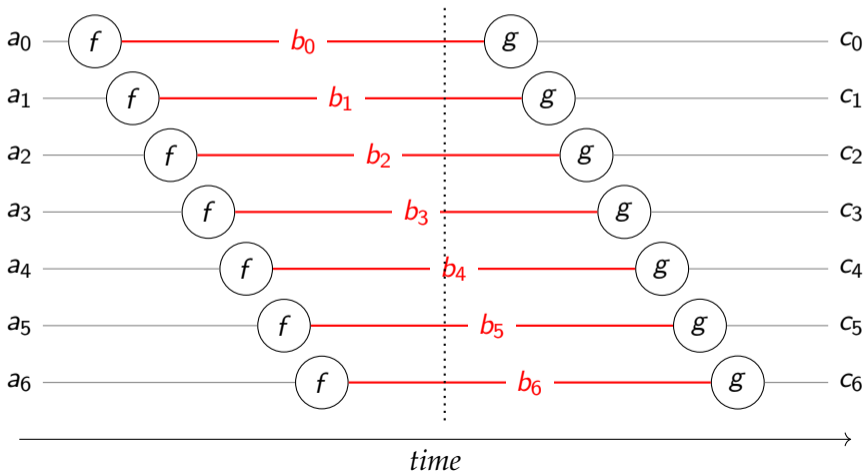
## Scheduling bitsliced code

```
// Suppose f: b1 -> b1 and g: b1 -> b1
node my_cipher (a:b7) returns (c:b7)
let   b = f(a);
      c = g(b);   tel
```



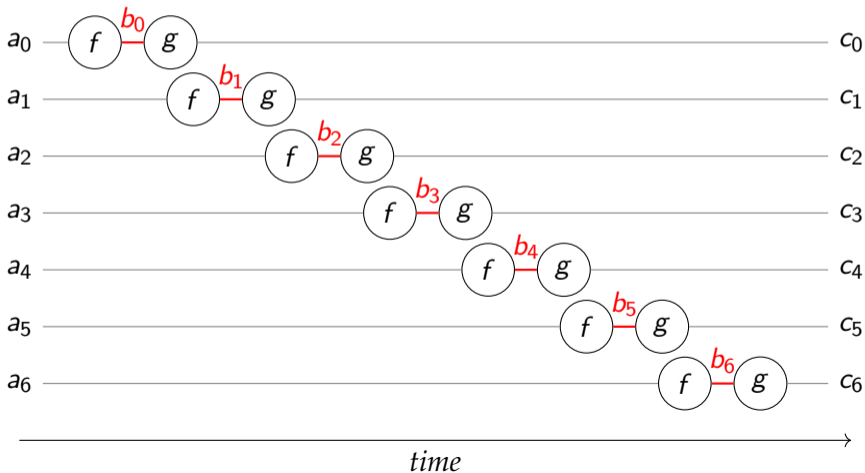
# Scheduling bitsliced code

```
// Suppose f: b1 -> b1 and g: b1 -> b1  
node my_cipher (a:b7) returns (c:b7)  
let   b = f(a);  
      c = g(b);   tel
```



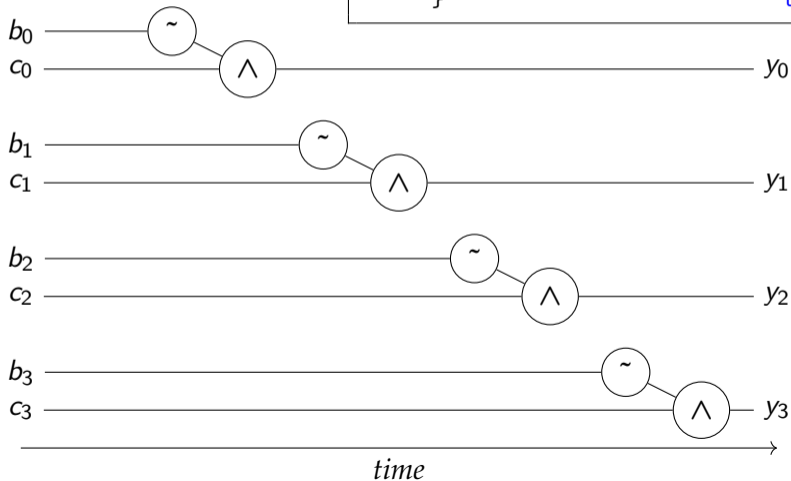
# Scheduling bitsliced code

```
// Suppose f: b1 -> b1 and g: b1 -> b1  
node my_cipher (a:b7) returns (c:b7)  
let   b = f(a);  
      c = g(b);   tel
```



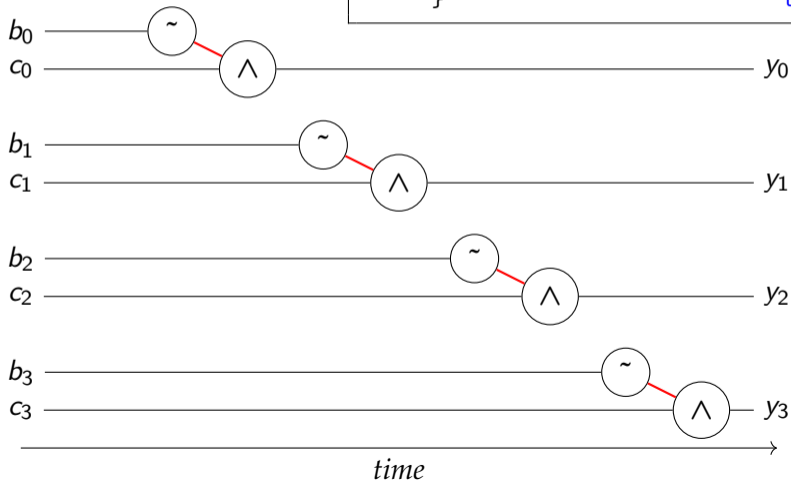
## Scheduling $m$ -sliced code

```
node my_cipher (a,b:b4) returns (y:b4)
let forall i in [0, 3] {
    tmp = ~ a[i];
    y[i] = tmp ^ b[i];
}
tel
```



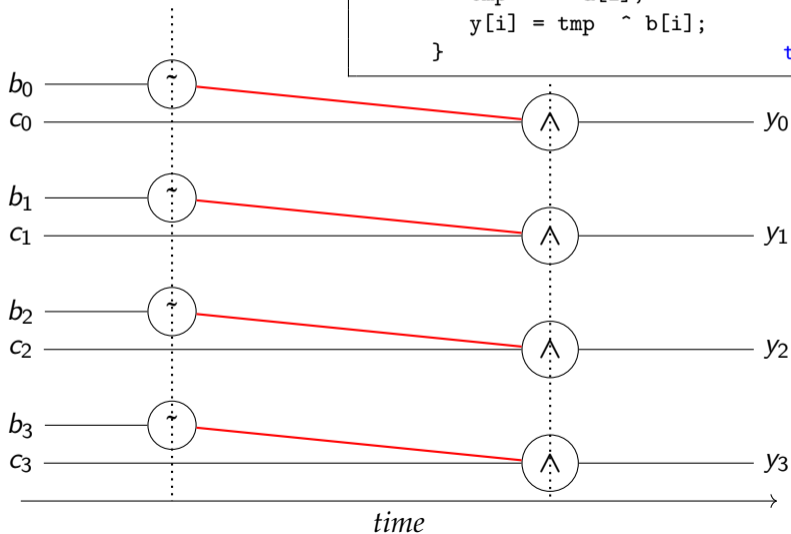
# Scheduling $m$ -sliced code

```
node my_cipher (a,b:b4) returns (y:b4)
let forall i in [0, 3] {
    tmp = ~ a[i];
    y[i] = tmp ^ b[i];
}
tel
```



# Scheduling $m$ -sliced code

```
node my_cipher (a,b:b4) returns (y:b4)
let forall i in [0, 3] {
    tmp = ~ a[i];
    y[i] = tmp ^ b[i];
}
tel
```





# Evaluation

## Scalability

